

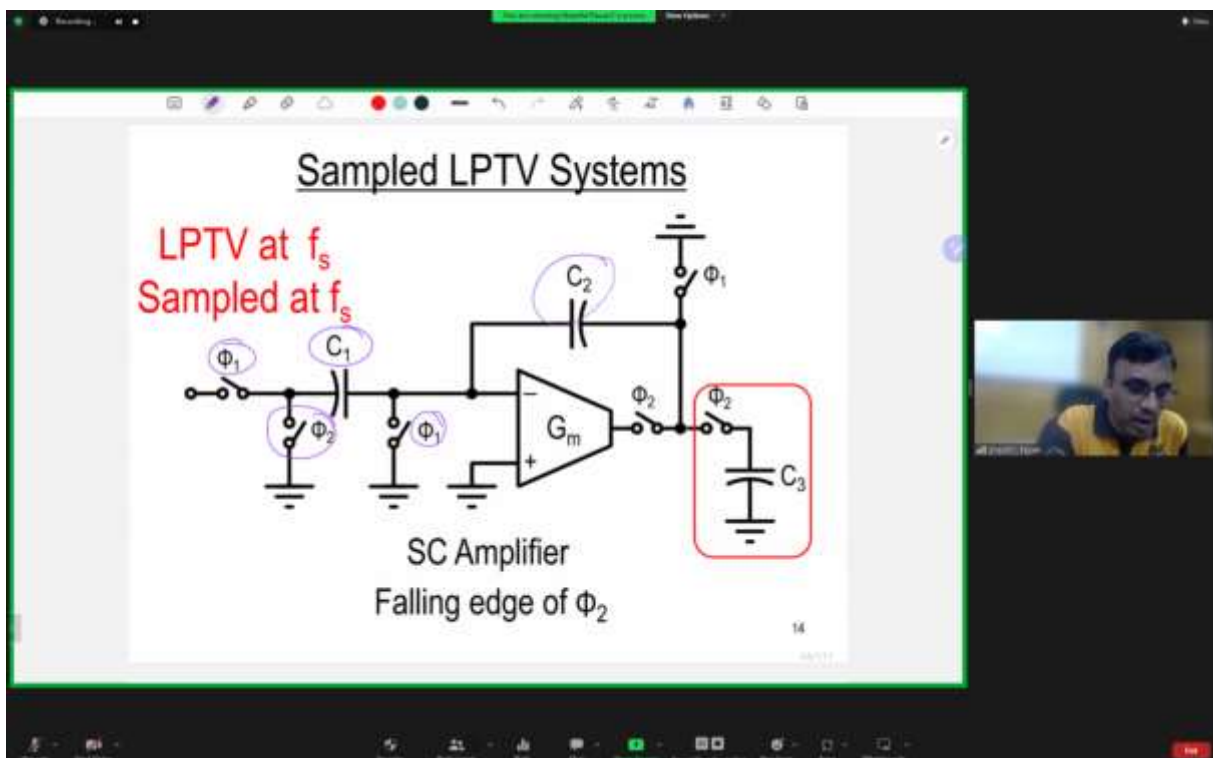
# Filter Design Course Sets New Record

The [\*“Practical Filter Design Techniques”\*](#) live-virtual short-course, held in May 2023, saw a global online audience of 100 analog design engineers and postgraduate students from a record number of countries, having the privilege of the highly interactive and intuition-based teaching sessions of Prof. Shanthi Pavan (IIT-Madras), gaining valuable insights of the key aspects of integrated CT filters, powerful concepts of reciprocity & inter-reciprocity, as well as analysis and applications of Linear Periodically Time-Varying (LPTV) systems.

A total of 26 countries, across 6 continents and 18 time-zones, were represented at this international course: Australia, Japan, S. Korea, Singapore, India, Iran, S. Arabia, Israel, Egypt, Greece, Austria, Germany, Poland, Netherlands, Switzerland, Czech Rep., Spain, Portugal, England, Scotland, Ireland, Canada, United States, Brazil, Chile and Argentina.

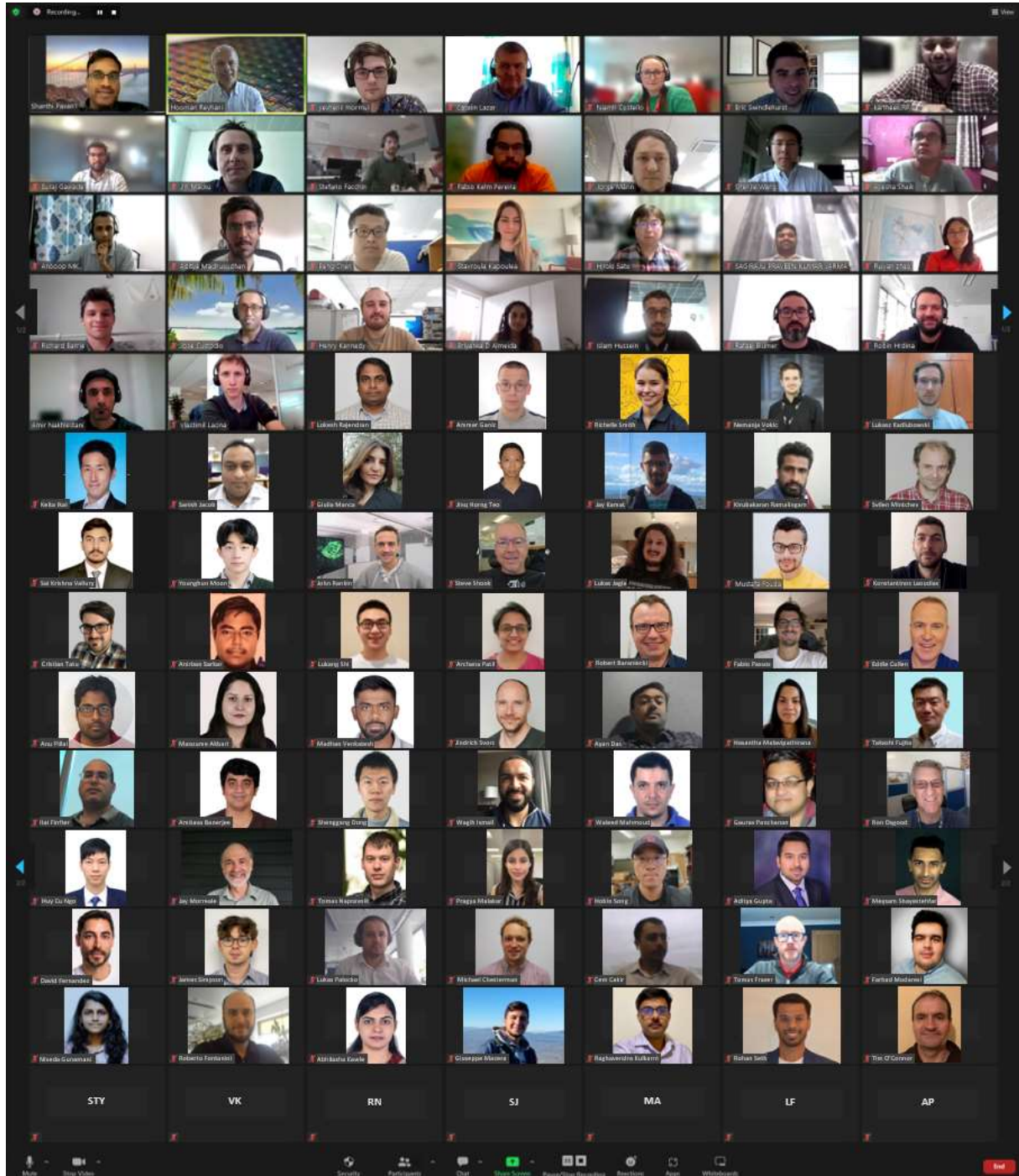
The main topics included Integrated Continuous-Time Filters (Part I & II); Noise in Electronic Circuits; Linear Periodically Time-Varying (LPTV) Circuits and Systems (Part I & II); LPTV Circuits - Case Studies; N-path Filters; Practical Design/Layout Tips.

The participants were continuously challenged by Prof. Pavan with probing questions on the course content being presented. This interactive teaching/learning approach achieved significantly higher level of understanding of the various concepts under discussion. The optional homework assignments consolidated even further the knowledge gained from the live-virtual sessions.



Prof. Shanthi Pavan (IIT-Madras), course presenter, talked about *“Practical Filter Design Techniques”* at an online course hosted by Hooman Reyhani, Ireland.

Prof. Pavan has been with the Indian Institute of Technology - Madras since 2002, where he is now a Professor of Electrical Engineering. He was previously with Texas Instruments (New Jersey) and Bigbear Networks (Sunnyvale). His research interests are in the areas of high-speed analog circuit design and signal processing. He is a co-author of "Understanding Delta-Sigma Data Converters" (2<sup>nd</sup> Edition), winner of Wiley-IEEE Press Professional Book Award in 2020. Dr. Pavan has served as a Distinguished Lecturer for the IEEE SSCS, on the TPC of the ISSCC and Editor-in-Chief of IEEE TCAS-I. He is a Fellow of the IEEE.



The lecturer, organizer and many of the participants of the *"Practical Filter Design Techniques"* online course, May 2023.

The feedback from the course participants was of great endorsement. One participant commented, *"The presenter's knowledge was truly impressive and the delivery was excellent, keeping the audience engaged and attentive"*. Another wrote, *"The course format is ideal. The lecture playback facility was very beneficial as it helps revising the lecture in case missed some parts of the sessions. The homework and solutions support the understanding of the course material"*. While another said, *"Thank you for organizing these amazing courses and look forward to the next course"*.

Full access to this course content, as well as our previous courses, may be requested (subject to payment) via [here](#). For more information, [please see here](#).

— Hooman Reyhani